



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Kwan-Yong LIM, et al.

Serial No.: 10/616,302

Group Art Unit: 2815

Filed: July 10, 2003

Examiner: Brock II, Paul E

For: STACK GATE ELECTRODE SUPPRESSED WITH INTERFACE-REACTION AND
METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING THE SAME

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PETITION FOR EXTENSION OF TIME

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 12, 2004

Sir:

Applicant hereby petitions that an extension of the term necessary to render timely the filing of a response in the above-identified application be granted. Costs under 37 CFR 1.17(a)-(d) for such extension(s) and/or any other fee due with this response, not fully covered by the enclosed check in the amount of \$110.00, may be charged to our Deposit Account No.50-3226.

Respectfully submitted,

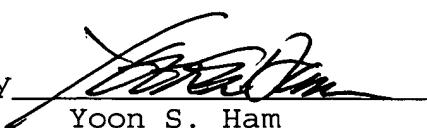
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